

Sri Venkateswara University, Tirupati

Model Syllabus for 4-Year UG Honours in B.Sc. (Electronics) as Major in
consonance with Curriculum framework w.e.f. AY 2025-26

COURSE STRUCTURE (for Semester I to VI)

Year	Semester	Course	Title of the Course	No. of Hrs /Week	No. of Credits
I	I	1	Fundamental of Electricity and Electronics	3	3
			Fundamental of Electricity and Electronics Practical Course	2	1
		2	Circuit theory and Electronic Devices	3	3
			Circuit theory and Electronic Devices Practical Course	2	1
	II	3	Semiconductor Devices and Materials	3	3
			Semiconductor Devices and Materials Practical Course	2	1
		4	Digital Electronics	3	3
			Digital Electronics Practical Course	2	1
II	III	5	Analog Circuits and Communication	3	3
			Analog Circuits and Communication Practical Course	2	1
		6	Microprocessor System	3	3
			Microprocessor System Practical Course	2	1
		7	Electrical and Electronics Instrumentation	3	3
			Electrical and Electronics Instrumentation Practical Course	2	1
	IV	8	Electronic Communication System	3	3
			Electronic Communication System Practical Course	2	1
		9	Microcontroller and Interfacing	3	3
			Microcontroller and Interfacing Practical Course	2	1
		10	Computer Network	3	3

Year	Semester	Course	Title of the Course	No. of Hrs /Week	No. of Credits
			Computer Network Practical Course	2	1
III	V	11	Cellular Mobile Communication	3	3
			Cellular Mobile Communication Practical Course	2	1
		12 A	Industrial Electronics	3	3
			Industrial Electronics Practical Course	2	1
		OR			
		12 B	Embedded System Design	3	3
			Embedded System Design Practical Course	2	1
		13 A	Digital System Design	3	3
			Digital System Design Practical Course	2	1
		OR			
		13 B	Consumer Electronics	3	3
			Consumer Electronics Practical Course	2	1
	VI	14 A	Power Electronics	3	3
			Power Electronics Practical Course	2	1
		OR			
		14 B	VLSI Design	3	3
			VLSI Design Practical Course	2	1
		15 A	Principles and Utility of Electronic Domestic Applications	3	3
			Principles and Utility of Electronic Domestic Applications Practical Course	2	1
		OR			
		15 B	Digital Signal Processing	3	3
			Digital Signal Processing Practical Course	2	1

Note: In the III Year (during the V and VI Semesters), students are required to select a pair of electives from one of the Two specified domains. For example: if set 'A' is chosen, courses 12 to 15 to be chosen as 12 A, 13 A, 14 A and 15 A or if set 'B' is chosen, It is to be chosen as 12 B, 13 B, 14 B and 15 B to ensure in-depth understanding and skill development in the chosen domain, students must continue with the same domain electives in both the V and VI Semesters.

T. Ashuraman

SEMESTER - I

COURSE 2: CIRCUIT THEORY AND ELECTRONIC DEVICES

Theory

Credits: 3

3 hrs/week

Course Objectives :

1. To explain the basic concepts and laws of DC and AC electrical networks and solve them using mesh and nodal analysis techniques.
2. To analyze circuits in time and frequency domain.
3. To synthesize the networks using passive elements.
4. To understand the construction, working and VI characteristics of electronic devices.
5. To understand the concept of power supply.

UNIT- 1: (9 Hrs)

SINUSOIDAL ALTERNATING WAVEFORMS:

Definition of current and voltage. The sine wave, general format of sine wave for voltage or current, phase relations, average value, effective (R.M.S) values. Differences between A.C and D.C. Phase relation of R, L and C

UNIT-II: (9 Hrs)

PASSIVE NETWORKS AND NETWORKS THEOREMS (D.C):

Branch current method, Nodal Analysis, star to delta & delta to star conversions. Superposition Theorem, Thevenin's Theorem, Norton's Theorem, Maximum Power, Milliman and Reciprocity theorems .

UNIT-III: (9 Hrs)

RC, RL AND RLC CIRCUITS:

Frequency response of RC and RL circuits, their action as low pass and high pass filters. Passive differentiating and integrating circuits. Series resonance and parallel resonance circuits, Q – Factor,

UNIT-IV: (9 Hrs)

BJT, FET and UJT:

BJT: Construction, working, and characteristics of CE Configurations. Hybrid parameters and hybrid equivalent circuit of CE Transistor,

FET: Construction, working and characteristics of JFET and MOSFET. Advantages of FET over BJT.

UJT: Construction, working and characteristics of UJT. UJT as a Relaxation oscillator.

UNIT-V: (9 Hrs)

POWER SUPPLIES & PHOTO ELECTRIC DEVICES

Rectifiers: Half wave ,full wave rectifiers-Efficiency-ripple factor- Filters- L- section & π -section filters. Three terminal fixed voltage I.C.regulators (78XX and &79XX). Light Emitting Diode – Photo diode and LDR.

TEXT BOOKS:

1. Introductory circuit Analysis (UBS Publications)----- Robert L. Boylestad.
2. Electronic Devices and Circuit Theory ----- Robert L. Boylestad & Louisashelsky.
3. Circuit Analysis by P.Gnanasivam- Pearson Education
4. Electronic Devices and Circuit Theory ----- Robert L. Boylestad & Louis Nashelsky.
5. Electronic Devices and Circuits I – T.L.Floyd- PHI Fifth Edition

REFERENCE BOOKS:

1. Engineering Circuit Analysis By: Hayt & Kemmerly - MG.
2. Networks and Systems – D.Roy Chowdary.
3. Unified Electronics (Circuit Analysis and Electronic Devices)
by Agarwal- Arora
4. Electric Circuit Analysis- S.R. Paranjothi- New Age International.
5. Integrated Electronics – Millmam & Halkias.
6. Electronic Devices & Circuits – Bogart.
7. Sedha R.S., A Text Book Of Applied Electronics, S.Chand & Company Ltd

SEMESTER - I

COURSE 2: CIRCUIT THEORY AND ELECTRONIC DEVICES

Practical

Credits: 1

2 hrs/week

Course Outcomes:

1. Apply concepts of electric network topology, nodes, branches, loops to solve circuit problems including the use of computer simulation.
2. Apply time and frequency concepts of analysis.
 - a. Synthesize the network using passive elements.
 - b. Know about amplifier circuits, switching circuits and oscillator circuits their design and use in electronics.
 - c. Design and construction of a power supply.

List of Experiments :

1. Thevenin's Theorem-verification
2. Norton's Theorem-verification
3. Maximum Power Transfer Theorem-verification
4. LCR series resonance circuit.
5. BJT input and output characteristics
6. FET Output and transfer characteristics
7. UJT VI characteristics
8. LDR characteristics
9. IC regulated power supply(IC-7805)

Lab experiments are to be done on breadboard and simulation software and output values are to be compared and justified for variation.

T. Raghuraman

4- Year UG Honours, Major
Syllabus
(2025-26)
Semester-I

Model Question Paper

Time : 3hrs

Marks:75

Part-A

Answer any Five Questions. Each Question carries 3 Marks.

5x3=15

- 1.
- 2.
- 3.
- 4.
- 5.
- 6.
- 7.
- 8.
- 9.
- 10.

Answer the following questions. Each question carries 12 Marks

5x12=60

11. a) Unit-I
(Or)
b) Unit-I

12. a) Unit-II
(Or)
b) Unit-II

13. a) Unit-III
(Or)
b) Unit-III

14. a) Unit-IV
(Or)
b) Unit-IV

15. a) Unit-V
(Or)
b) Unit-V